IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re	U.S. Patent Application of)
OKU	DA))
Appli	cation Number: To Be Assigned))
Filed:	: Concurrently Herewith))
For:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, DATA PROCESSING SYSTEM AND MEMORY SYSTEM	,)))
Атто	PRNEY DOCKET NO. HITA.0652)

Honorable Assistant Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified National Stage U.S. patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

CERTIFICATION

This Information Disclosure Statement is submitted with the initial filing of the application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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December 20, 2004

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Form PTO 1449		ATTY. DOCKET NUMBER HITA.0652			Serial Number 0 15 18 4 3 1 To be Assigned				
	Department of Commerce at and Trademark Office		APPLICANT OKUDA						
Information D	isclosure Statement by Applicant		FILING DATE Concurrently Herewith			GROUP			
		U.S. P	atent Doci	uments					
Examiner Initial	DOCUMENT NUMBER	DATE	Name		CLASS	SUB FILING DATE CLASS)ATE	
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	International Search				r criment r age				
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DATE CONSIDERED

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EXAMINER